

### **In the Specification:**

Please replace the indicated paragraph(s) with the following clean replacement paragraph(s). A marked-up version showing amendments to the specification paragraph(s) being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 CFR § 1.121(b)(1)(iii).

At page 1, the paragraph inserted before the "Technical Field" by the amendment dated September 18, 2001, has been replaced with the paragraph shown below:

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### **CROSS REFERENCE TO RELATED APPLICATION**

BI This patent application is a Continuation Application of U.S. Patent Application Serial No. 09/765,510, filed on January 19, 2001, now U.S. Patent No. 6,309,941, entitled "Capacitors, DRAM Arrays, Monolithic Integrated Circuits, And Methods of Forming Capacitors, DRAM Arrays, And Monolithic Integrated Circuits", naming Kunal R. Parekh, John K. Zahurak and Phillip G. Wald as inventors, which is a Continuation Application of U.S. Patent Application Serial No. 08/887,742, filed on July 3, 1997, now U.S. Patent No. 6,207,523, the disclosures of which are incorporated by reference.

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### **In the Claims**